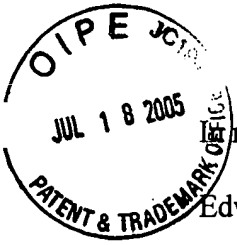


AF/JPW

10/708,586



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inter patent application of:

Edward J. Nowak

Serial No.: 10/708,586

Examiner: Hu Shouxiang

Filed: March 12, 2004

Group Art Unit: 2811

Atty. Docket No.: BUR920030159US1

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on July 14, 2005

Frederick W. Gibb, III

For: HIGH-PERFORMANCE CMOS SOI DEVICES ON HYBRID CRYSTAL-ORIENTED SUBSTRATES

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action mailed May 18, 2005, please amend the above-identified patent application as follows: